



Attorney's Docket No.: 10559-606001/P12890

2123

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : William R. Wheeler et al. Art Unit:
Serial No.: 10/007,007 Examiner:
Filed : December 4, 2001
Assignee : Intel Corporation
Title : PROVIDING MODELING INSTRUMENTATION WITH AN
APPLICATION PROGRAMMING INTERFACE TO A GUI
APPLICATION

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This filing is being made before the receipt of a first
Office action on the merits. No fee is required.

The documents are in the English language; hence no concise
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Serial No.: 10/007,007
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Consideration of the foregoing and enclosures plus the return of a copy of the enclosed form PTO-1449 with the Examiner's initials in the left column per MPEP 609 are earnestly solicited along with an early action on the merits.

Please apply any additional charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

Date: 3/18/04



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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-606001	Application No. 10/007,007
Information Disclosure Statement by Applicant (Use several sheets if necessary) (37 CFR §1.98(b))		Applicant William R. Wheeler et al.	
		Filing Date December 4, 2001	Group Art Unit

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U.S. Patent Documents

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA	4,970,664	11/1990	Kaiser, et al.			
	AB	5,212,650	05/1993	Hooper, et al.			
	AC	5,267,175	11/1993	Hooper			
	AD	5,278,769	01/1994	Bair, et al.			
	AE	5,297,053	03/1994	Pease, et al.			
	AF	5,301,318	04/1994	Mittal			
	AG	5,384,710	01/1995	Lam, et al.			
	AH	5,493,507	02/1996	Shinde, et al.			
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	AK	5,604,894	02/1997	Pickens, et al.			
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(37 CFR §1.98(b))

U.S. Patent Documents

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	ABB	6,353,806	03/2002	Gehlot			
	ACC	6,360,356	03/2002	Eng			
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Foreign Patent Documents or Published Foreign Patent Applications

Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
	ATT	0 404 482	12/1990	EPO				
	AUU	0 433 066	06/1991	EPO				
	AVV	0 901 088	03/1999	EPO				
	AWW	1 065 611	01/2001	EPO				
	AXX	03-225523	10/1991	Japan				
	YYY	08-314892	11/1996	Japan				

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							Yes	No
	AZZ	WO 98/37475	08/1998	WIPO				
	AAAA	WO 98/55879	12/1998	WIPO				
	ABBB	WO 99/39268	08/1999	WIPO				
	ACCC	WO 00/65492	11/2000	WIPO				

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Examiner Initial	Desig. ID	Document
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	AEEE	Lahti, et al., "SADE: a Graphical Toll for VHDL-Based System Analysis", 1991 IEEE International Conference on Computer-Aided Design, November 11, 1991, pp. 262-265.
	AFFF	Lin, et al., "A Goal Tree Based High-Level Test Planning System for DSP Real Number Models", 1998 Proceedings of International Test Conference, October 18, 1998, pp. 1000-1009.
	AGGG	Maxfield, C., "Digital Logic Simulation: Event-Driven, Cycle-Based, and Home-Brewed", <i>Electrical Design News</i> , 41(14):129-136 (1996).
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	ALLL	Su, Stephen, "An Interactive Design Automation System", <i>Proceedings of the 10th Design Automation Workshop on Design Automation</i> , pp. 253-261, June 1973
	AMMM	Yli-Pietila, et al., "The Design and Simulation of Complex Multitechnology Systems", IEEE International Conference on Systems Engineering, August 9, 1990, pp. 474-477.

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